


Paper # 11

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Graphical Programming System with Distributed Block Diagram Execution and Front Panel Display						
Application Number:		09/617600					
Confirmation Number:		6393			RECEIVED NOV 05 2003 Technology Center 2100		
First Named Applicant:		Robert Dye					
Attorney Docket Number:		5150-38601					
Art Unit:		2122					
Examiner:		Ted T. Vo					
Search string:		(6230307 or 6226776 or 5555201 or 6219628 or 5684980 or 5535342 or 5652875 or 5497498 or 5583749 or 5541849 or 5737235 or 5638299 or 5309556 or 6064409 or 5732277 or 4827404 or 5652909 or 5481740 or 5377318 or 6173438).pn.					
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
TTU	1	6230307	2001-05-08	Davis et al.			
TTU	2	6226776	2001-05-01	Panchul et al.			
TTU	3	5555201	1996-09-10	Dangelo et al.			
TTU	4	6219628	2001-04-17	Kodosky et al.			
TTU	5	5684980	1997-11-04	Casselman			
TTU	6	5535342	1996-07-09	Taylor			
TTU	7	5652875	1997-07-29	Taylor			
TTU	8	5497498	1996-03-05	Taylor			
TTU	9	5583749	1996-12-10	Tredennick et al.			
TTU	10	5541849	1996-06-30	Rostoker et al.			
TTU	11	5737235	1998-04-07	Kean et al.			
TTU	12	5638299	1997-06-10	Miller			
TTU	13	5309556	1994-05-03	Sismilich			
TTU	14	6064409	2000-05-16	Thomsen et al.			

TTV	15	5732277	1998-03-24	Kodosky et al.
TTV	16	4827404	1989-05-02	Barstow et al.
TTV	17	5652909	1997-07-29	Kodosky
TTV	18	5481740	1996-01-02	Kodosky
TTV	19	5377318	1994-12-27	Wolber
TTV	20	6173438	2001-01-09	Kodosky et al.

Remarks

Note: Remarks are not for responding to an office action.

Other art will be submitted on a Form 1449.

Signature

Examiner Name	Date
TED T. VO	1/16/4

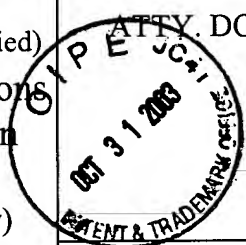
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Form PTO-1449 (modified)

List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DOCKET NO: 5150-38601

SERIAL NO: 09/617,600



APPLICANT: Dye et al.

FILING DATE: June 13, 2000

GROUP: 2122

FOREIGN PATENT DOCUMENTS

EXAM. INITIAL S	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO N. YES/NO
TIV		WO 94 10627 A	5/11/94	PCT			
TIV		WO 94 15311 A	7/7/94	PCT			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TIV		De Coster, GRAPE-II: An Introduction [online]. Automatic Control and Computer Architectures Department. Katholieke Universiteit Leuven, Belgium, February 22, 1996 [retrieved October 6, 1999] Retrieved from the Internet @ http://www.esat.kuleuven.ac.be/acca
TIV		Collamati et al. "Induction Machine stator Fault On-line Diagnosis Based on LabVIEW Environment", Mediterranean Electrotechnical Conference, Vol. 1, pg. 495-498, May 1996.
TIV		Spoelder et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation", Proc. IEEE Instr. and Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997.
TIV		Srinivasan et al., "LabVIEW program Design for On-Line Data Acquisition and Predictive Maintenance", Proc. Of the 30th Southeastern Symp. On System Theory, pp. 520-524, March 1998.
TIV		Wahidanabanu et al., "Virtual Instrumentation with Graphical Programming for Enhanced Detection and Monitoring of Partial Discharges", Proc. Electrical Insulation Conf. 1997, pp. 291-296, September 1997.
TIV	B1	Choosing Block-Diagram Tools for DSP Design, http://www.bdti.com/articles/info_dspmt95blockdiagram.htm , May 9, 2003, pgs. 1-7.
TIV	B2	Real-Time Workshop for Use with Simulink, User's Guide, May 1994, 229 pages.
TIV	B3	Guide to Rapid Prototyping with Simulink, Real-Time Workshop and dSPACE, 1995, 16 pages.
TIV	B4	Real-Time Interface to Simulink, RTI 30, User's Guide, 1995, 125 pages.
TIV	B5	Kevin J Gorman and Kourosh J. Rahnamai, "Real-Time Data Acquisition and Controls Using MatLAB", 1995, 4 pages.
TIV	B6	SPW - MatLAB Co-Simulation Interface Product Data Sheet, 1996, 2 pages.
TIV	B7	Signal Processing WorkSystem, MatLAB Interface User's Guide, Oct. 1995, 72 pages.
TIV	B8	Alta Group of Cadence Design Systems, Inc., 1995, 34 pages.
TIV	B9	Code Generation System Product Data Sheet, 1994, 8 pages.

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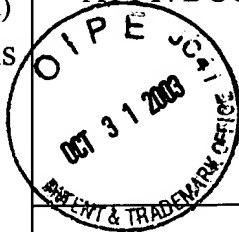
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<input checked="" type="checkbox"/>	B10	SPW/CGS Porting Kits Product Data Sheet, 11/94, 2 pages.	RECEIVED
<input checked="" type="checkbox"/>	B11	MultiProx for SPW Product Data Sheet, 08/94, 4 pages.	NOV 04 2003
<input checked="" type="checkbox"/>	B12	DSP ProCoder for SPW Product Data Sheet, 11/94, 4 pages.	
<input checked="" type="checkbox"/>	B13	Xanalog Corporation Sales Manual, January 1987, 8 pages.	Technology Center 2100
<input checked="" type="checkbox"/>	B14	Available XA-1000 Literature and Its Use, 1986, 2 pages.	
<input checked="" type="checkbox"/>	B15	Xanalog, XA-1000 Programming ICONS, 1986, 26 pages.	
<input checked="" type="checkbox"/>	B16	Xanalog's CAE System: The Fastest AT Alive, Mass High Tech, Vol. 4, No. 22, 08/1988, 1 page.	
<input checked="" type="checkbox"/>	B17	Xanalog The Computer Aided Engineering Workstation Comes to Simulation, Simulation Vol. 47, No. 1, July 1986, 3 pages.	
<input checked="" type="checkbox"/>	B18	Xanalog RT Real Time Analog and Digital I/O, 10/90, 4 pages.	
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<input checked="" type="checkbox"/>	B22	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 11/7/1988, 13 pages.	
<input checked="" type="checkbox"/>	B23	Lee et al., "A Design Tool for Hardware and Software for Multiprocessor DSP Systems", May 1989, 4 pages.	
<input checked="" type="checkbox"/>	B24	Gabriel 0.7 Overview, 1990, 5 pages.	
<input checked="" type="checkbox"/>	B25	Joseph T. Buck and Edward A. Lee, "Scheduling Dynamic Dataflow Graphs with Bounded Memory Using the Token Flow Model", http://www.synopsys.com/ , 1995, 4 pages.	
<input checked="" type="checkbox"/>	B26	Edward A. Lee, "Design Methodology for DSP", 1992, 4 pages.	
<input checked="" type="checkbox"/>	B27	Pino et al, "Interface Synthesis in Heterogeneous System-Level DSP Design Tools, 05/1996, 4 pages.	
<input checked="" type="checkbox"/>	B28	Jose Luis Pino, Master's Report, "Software Synthesis for Single-Processor DSP Systems Using Ptolemy", May 1993, 48 pages.	
<input checked="" type="checkbox"/>	B29	Asawaree Kalavade and Edward A. Lee, "A Hardware-Software Codesign Methodology for DSP Applications", 1993, 12 pages.	
<input checked="" type="checkbox"/>	B30	Pino et al., "Automatic Code Generation for Heterogeneous Multiprocessors", 1994, 4 pages.	
<input checked="" type="checkbox"/>	B31	Tool Chest continues to Grow, Electronic Engineering Times, 12/15/1995, 2 pages.	
<input checked="" type="checkbox"/>	B32	Pino et al., "Mapping Multiple Independent Synchronous dataflow Graphs onto Heterogeneous Multiprocessors, 10/1994, 6 pages.	
<input checked="" type="checkbox"/>	B33	Asawaree Kalavade and Edward A. Lee, "Hardware/Software Co-Design Using Ptolemy - A Case Study, 09/1992, 18 pages.	
<input checked="" type="checkbox"/>	B34	Pino et al, "Software Synthesis for DSP Using Ptolemy", 1995, 15 pages.	
<input checked="" type="checkbox"/>	B35	Vol. 1 - Ptolemy 0.7 User's Manual, 1997, 532 pages.	
<input checked="" type="checkbox"/>	B36	i-Logix Product Overview, 1996, 52 pages.	
<input checked="" type="checkbox"/>	B37	Press Release, i-Logix Statemate MAGNUM Supports PCs", 01/31/1997, 2 pages.	
<input checked="" type="checkbox"/>	B38	Press Release, "i-Logix Signs Reseller Agreement for Virtual Prototypes, Inc.'s VAPS Product Line, 02/11/1997, 2 pages.	
<input checked="" type="checkbox"/>	B39	Press Release, "i-Logix Introduces Rhapsody, Object-Oriented analysis, Design and Implementation Tool", 02/10/1997, 2 pages.	

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TIV	B40	Statemate/C Product Overview, 1995, 4 pages.
TIV	B41	Press Release, "i-Logix and Integrated Systems Link Statemate MAGNUM and MATRIX AutoCode" 01/03/1997, 2 pages
TIV	B42	Press Release, "i-Logix and Wind River unveil Industry's First Rapid Prototyping Solution for Testing Embedded Systems at ESC West in San Jose, 09/17/1996, 3 pages.
TIV	B43	Press Release, "i-Logix Inc. Endorses Unified Modeling Language, 01/16/1997, 1 page.
TIV	D1	Philip Dean Lapsley, "Host Interface and Debugging of Dataflow DSP Systems", Thesis, 1991, pgs. 1-51.
TIV	D2	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 1988, pgs. 1-11.
TIV	D3	Lee et al., "Gabriel: A Design Environment for DSP", IEEE Transactions on Acoustics, Speech, and Signal Processing, Vol. 37, No. 11, Nov. 1989, pgs. 1751-1762.
TIV	D4	Bier et al., "Gabriel: A Design Environment for DSP", IEEE Micro Issue - Vol. 10, No. 5, 1990, pgs. 28-45.
TIV	U	Ludolph et al., "The Fabrik Programming Environment", IEEE, pages 222-230, 1988.

EXAMINER:

TED T. VO

DATE CONSIDERED:

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

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